

Lecture Note On Microprocessor And Microcontroller Theory

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The 8085 microprocessor is an 8-bit processor available as a 40-pin IC package and uses +5 V for power. It can run at a maximum frequency of 3 MHz. Its data bus width is 8-bit and address bus width is 16-bit, thus it can address 2¹⁶ = 64 KB of memory. The internal architecture of 8085 is shown in Fig. 2.

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Microprocessor is a semiconductor programmable device which fetches, decodes and executes instructions. The set of instructions may vary from processor to processor. In microprocessors, 3 buses are mainly used.

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Microprocessor as a programmable digital device, which can be used for both data processing and control applications. In view of a computer student, it is the CPU of a Computer or heart of the computer. A computer which is built around a microprocessor is called a microcomputer. A

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Microprocessor is a program-controlled device, which fetches the instructions from memory, decodes and executes the instructions. Most Micro Processors are single-chip devices. Microprocessor is a backbone of computer system, which is called CPU. Microprocessor speed depends on the processing speed depends on DATA BUS WIDTH.

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LECTURE 2:- It is the main register of microprocessor directly connected with the ALU. It is also called register 'A'. It is an 8-bit register. It is used in the arithmetic and logic operations. It always contains one of the operands on which arithmetic/logic has to be performed. After the arithmetic/logic operation, the contents of

Microprocessor and Programming - SDMP

Lecture Notes On Microprocessors and Microcomputer A Microprocessor is a multipurpose programmable logic device which reads the binary instructions from a storage device called **Memory** accepts binary data as input and processes data according to the instructions and gives the results as output.

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MicroProcessors Lecture Notes. This note covers the following topics: Introduction to embedded system, Design metrics, Definitions of general-purpose, single-purpose, and application-specific processors, Introduction to Nios II processor, Programming model, Instruction set categories, Instruction decoding, Two memory architecture, Instruction execution sequence, Superscalar and VLIW, Address modes.

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